GPIO Pins and Alternate Functions - Page 650 – 651

Interrupt Register Map – 135

IRQ - 104

Our Usage

Port D

|  |  |
| --- | --- |
| Pin | Use |
| 0 |  |
| 1 |  |
| 2 |  |
| 3 | PWM0 |
| 4 |  |
| 5 |  |
| 6 |  |
| 7 |  |

Port E

|  |  |
| --- | --- |
| Pin | Use |
| 0 | ADC Channel 3 | Sequencer2 | ADC0 |
| 1 | ADC Channel 2 | Sequencer2 | ADC0 |
| 2 | ADC Channel 1 | Sequencer2 | ADC0 |
| 3 | ADC Channel 0 | Sequencer2 | ADC0 |
| 4 |  |
| 5 |  |
| 6 |  |
| 7 |  |

Timer 2 -> Used to Trigger ADC